Electronic Acknowledgement Receipt FES ID: 1198369 Application Number: 10620492 Confirmation Number: 9069 Method to reduce transistor gate to source/drain overlap capacitance by Title of Invention: incorporaton of carbon First Named Inventor: Majid Movahed Mansoori Customer Number: 23494 Filer: Jacqueline J. Garner. Filer Authorized By: Attorney Docket Number: TI-35375 Receipt Date: 13-SEP-2006 Filing Date: 16-JUL-2003 Time Stamp: 11:50:56 Application Type: Utility International Application Number:

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages		
1		T35375_111_OA_442006.pd f	13917	yes	6		
	Multipart Description						
	Doc Desc		Start	End			
	Amendment - After Non-Final Rejection		1	1			
	Claims		2	5			
	Applicant Arguments/Remarks Made in an Amendment		6	6			
Warnings:							
Information:							
2	Fee Worksheet (PTO-875)	fee-info.pdf	8179	no	2		
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.